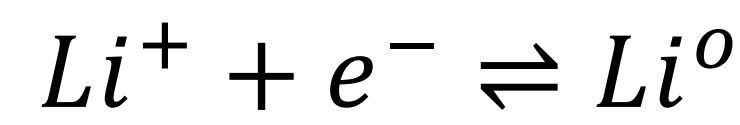


Lithium Plating

- Formation of metallic lithium on negative electrode surface:



- Plating thermodynamically possible if [1,2]:

$$\Phi_{\text{solid}} - \varphi_{\text{elec}} < 0$$

- Plating condition:

$$\eta_{\text{So/El}} + OCV_{\text{solid}}(\text{SoC}) < 0$$

1D+1D Model

- Based on Arora et al. [3]
- Lithium plating and stripping
- No intercalation of plated lithium into active particle
- Deposition current in negative electrode

$$i_d = i_{d,0}(c_e) \cdot \left(f(r_{\text{Li}}) \cdot \exp\left(\frac{\alpha_a \cdot F \cdot \eta_d}{R \cdot T}\right) - \exp\left(-\frac{\alpha_c \cdot F \cdot \eta_d}{R \cdot T}\right) \right)$$

$$\eta_d = \Phi_s - \varphi_e - i_d \cdot R_{\text{film}}$$

$$f(r_{\text{Li}}) = \begin{cases} 1 & r_{\text{Li}} > r_{\text{min}} \\ r_{\text{Li}}/r_{\text{min}} & r_{\text{Li}} \leq r_{\text{min}} \end{cases}$$

- Film resistance

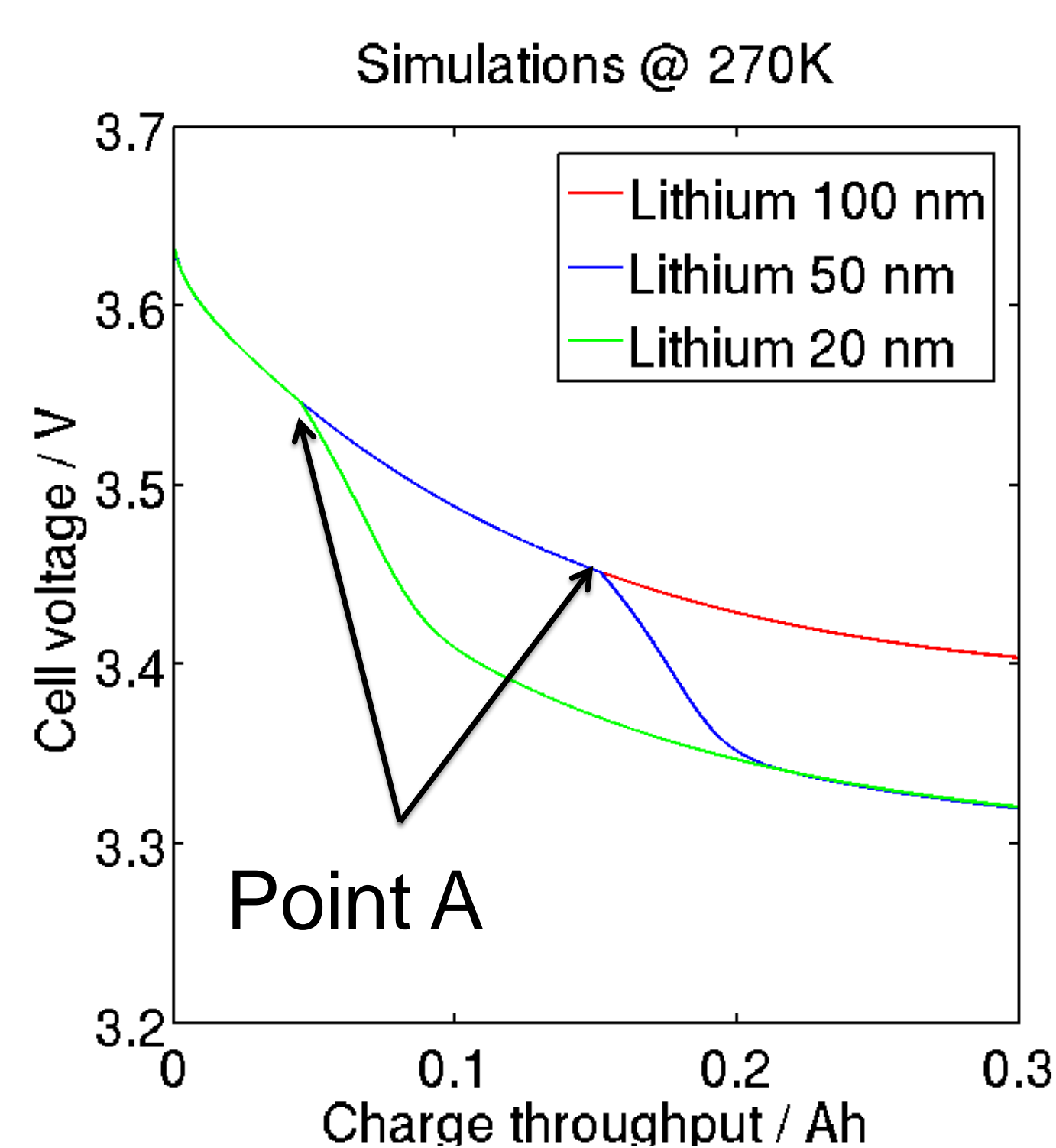
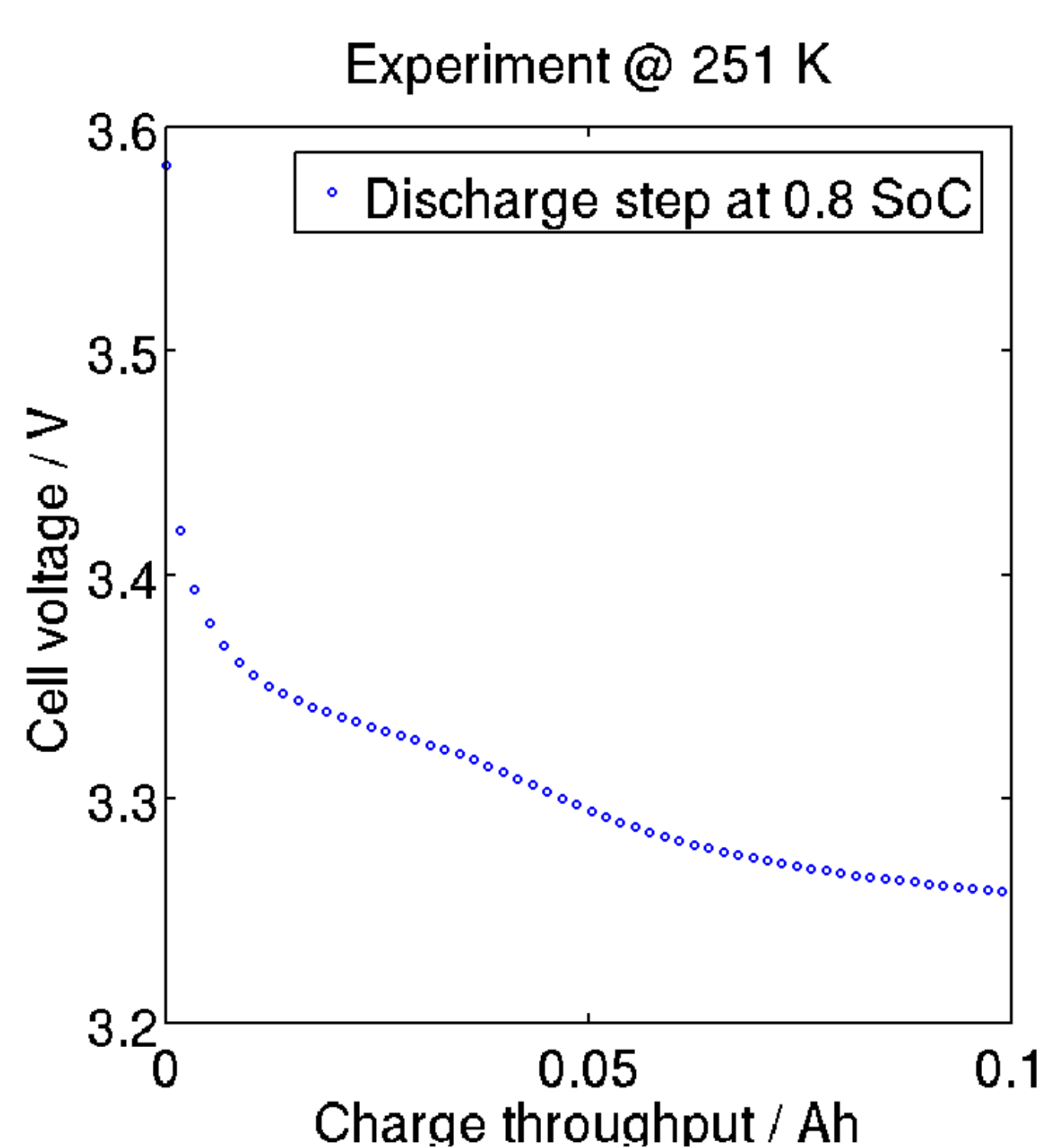
$$R_{\text{film}} = R_{\text{SEI}} + \frac{r_{\text{Li}}}{\sigma_{\text{Li}}}$$

- Film Growth

$$\frac{\partial r_{\text{Li}}}{\partial t} = -\frac{i_d \cdot M_{\text{Li}}}{\rho_{\text{Li}} \cdot F}$$

Comparison between Experiment and Simulation [4]

- Experiments on commercial cylindrical 26650-type cells
- LFP + Graphite
- Charge and discharge at low temperatures



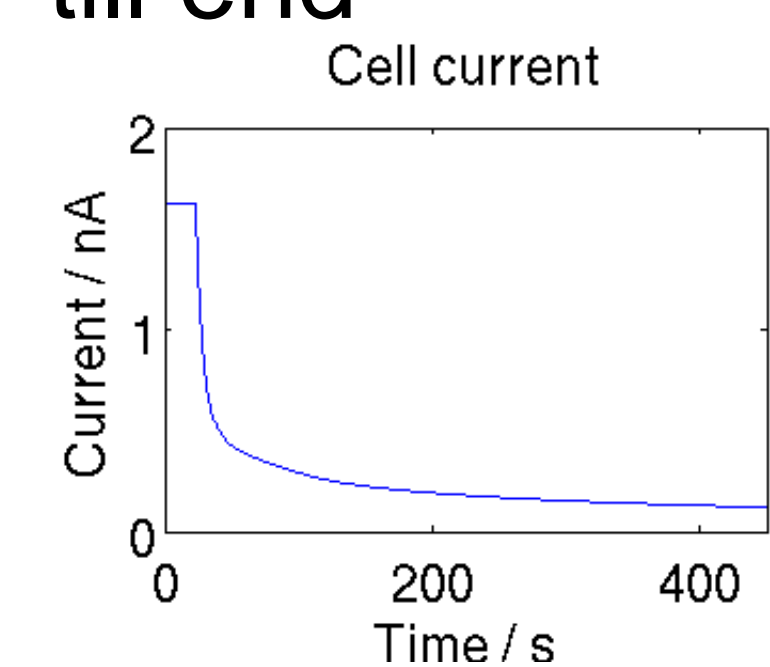
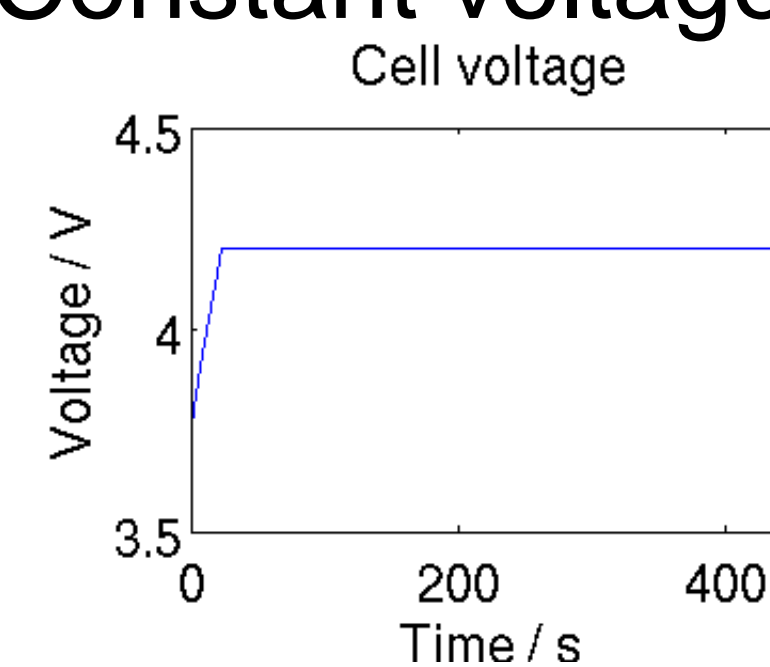
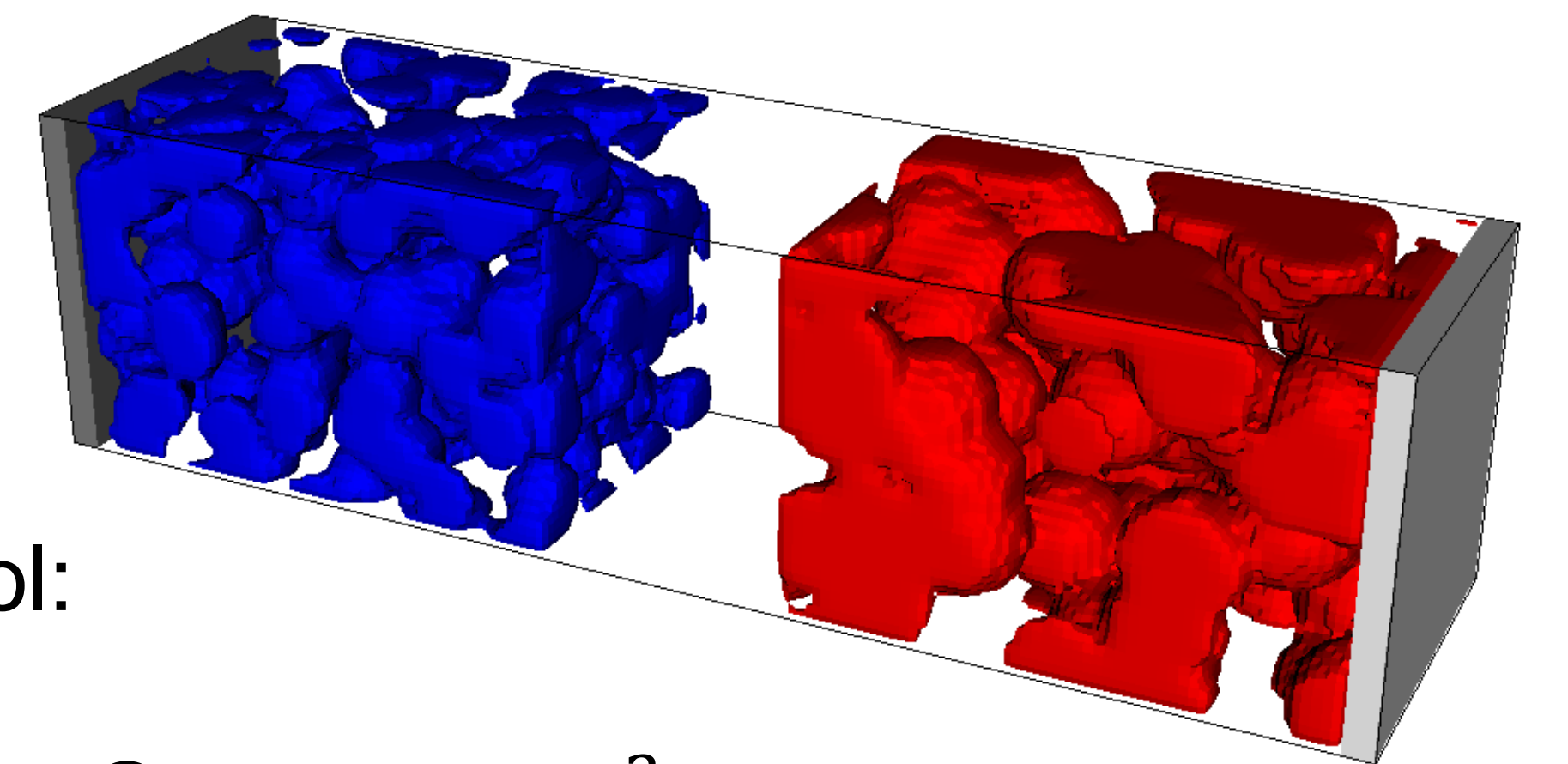
- Plateau in cell voltage due to lithium stripping
- Point A: Indication that most of the lithium is stripped
- Shape of Plateau: Kinetics of stripping
- No SEI-formation allowed
- Temperature dependence of OCP unknown

3D Model

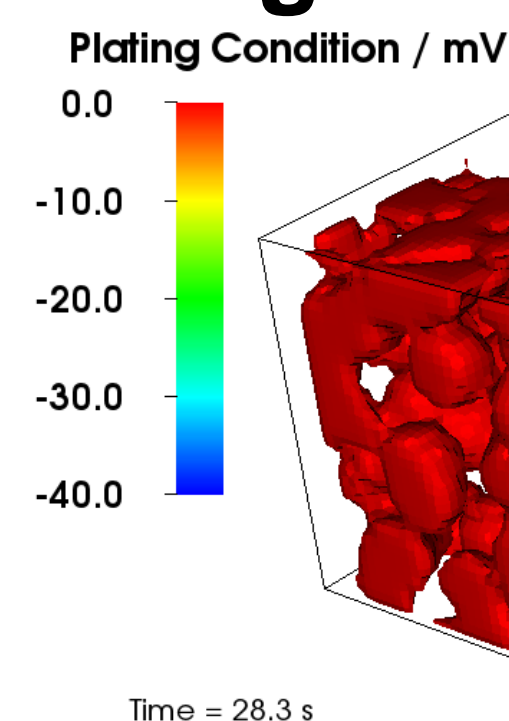
- Based on Latz et al. [5]
- Software framework BEST [6]

3D Microstructure

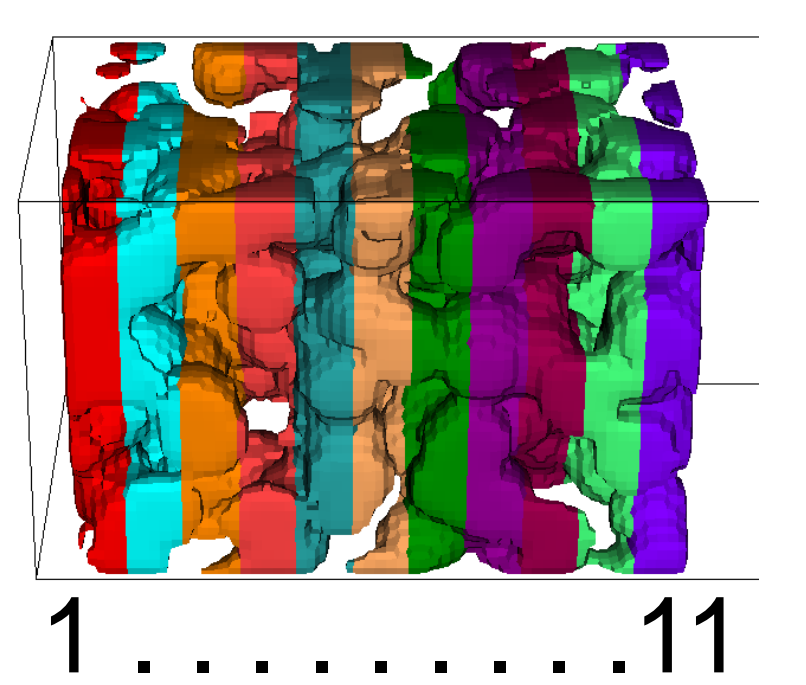
- Blue: Graphite
- Red: LMO
- Void: LiPF₆ (EC:DMC 3:7)
- Operation protocol:
 - $T = 298 \text{ K}$
 - Constant current @ 30 mA/cm^2 till $U_{\text{cell}} = 4.2 \text{ V}$
 - Constant voltage @ 4.2 V till end



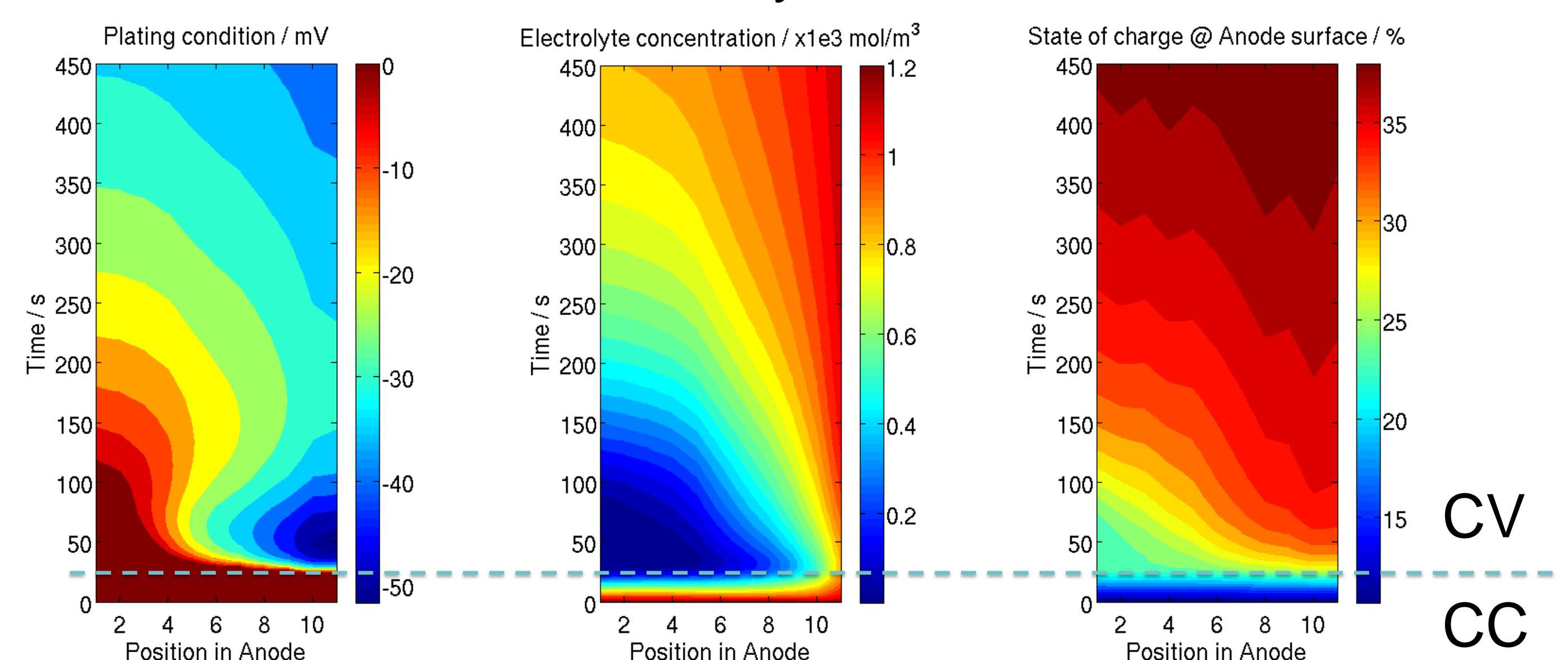
Plating Condition



- First fulfilled at Separator/Anode interface



- Subdivide Anode for analysis:



- Plating condition fulfilled at RT
- Li-Diffusion in Elyte hinders advancing of plating front:
 - Li-Depletion in Elyte → No further change of SoC
 - Li-Diffusion in solid lowers SoC on Surface
 - Li-Diffusion in Elyte → Advancing of plating front

- [1] N. Legrand, B. Knosp, P. Desprez, F. Lapique, and S. Raël, *J. Power Sources*, **245**, 208 (2014).
- [2] A. M. Colclasure and R. J. Kee, *Electrochim. Acta* **55** (28), 8960 (2010).
- [3] P. Arora, M. Doyle, and R. E. White, *J. Electrochem. Soc.* **146**, 3543 (1999).
- [4] M. Petzl and M. A. Danzer, *J. Power Sources* **254**, 80 (2014).
- [5] A. Latz and J. Zausch, *J. Power Sources* **196**, 3296 (2011).
- [6] ITWM, BEST, www.itwm.fraunhofer.de/best

Knowledge for Tomorrow

